

**IN THE CLAIMS:**

Please amend claims 2, 5, and 10 as follows:

1. (Cancelled)
2. (Currently Amended) A display device comprising:
  - signal lines which are formed on an upper surface side of a substrate to provide a display region;
  - an insulation film which is formed ~~such that the film~~ to cover[[s]] the signal lines except for terminal portions of the signal lines in periphery of the substrate; and
  - conductive layers which extend in an extension direction of the signal lines ~~such that the conductive layers~~ to traverse the terminal portions, wherein
    - a pair of gap[[s are]] ~~is formed on respective sides of each of the conductive layers parallel to the extension direction as well as between said each conductive layer and~~ in the insulation film and is shaped rectangular,
    - a pair of holes are formed in the signal lines at portions underneath and corresponding to two sides of the pair of gap[[s]] along the extension direction, and
    - [[said]] each of the conductive layers is formed on the signal lines and between the pair of holes, a part of said each conductive layer is formed on the insulating film, while the insulated film is formed on the signal lines and outside of the pair of holes.
3. (Original) A display device according to claim 2, wherein the display region includes gate signal lines and drain signal lines, wherein a material of the signal lines is equal to a material of the gate signal lines, and a material of the conductive layers is equal to a material of the drain signal lines.
4. (Original) A display device according to claim 2, wherein gate signal lines, drain signal lines and interlayer insulation films which are formed between the respective signal lines are formed on a display region, and a material of the insulation films is identical with a material of the interlayer insulation film.

5. (Currently Amended) A display device comprising:
- signal lines which are formed on an upper surface side of the substrate to provide a display region;
  - semiconductor layers which are formed below the signal lines by way of a first insulation film such that the semiconductor layers traverse the signal lines at terminal portions of the signal lines in a periphery of the substrate;
  - a second insulation film which is formed on top of the substrate to cover the signal lines and in which holes are formed above regions thereof where the semiconductor layers are formed;
  - conductive layers which have respective sides thereof in the extension direction of the signal lines arranged at both sides of the signal lines and are connected with respective semiconductor layers,
  - wherein the semiconductor layers are exposed at positions of the holes,
  - the display region includes thin film transistors, and
  - a material of the semiconductor layers in the periphery of the substrate is equal to a material of semiconductor layers of the thin film transistors in the display region.
6. (Original) A display device according to claim 5, wherein the display region includes gate signal lines and drain signal lines, wherein a material of the signal lines is equal to a material of the gate signal lines, and a material of the conductive layers is equal to a material of the drain signal lines.
- 7-8. (Cancelled)
9. (Original) A display device according to claim 5, wherein gate signal lines, drain signal lines and interlayer insulation films which are formed between the respective signal lines are formed on a display region and a material of the second insulation film is identical with a material of the interlayer insulation films.

10. (Currently Amended) A display device comprising:

signal lines which are formed on an upper surface side of a substrate to provide a display region;

a first insulation film which is formed between the substrate and the signal lines,

a[[n]] second insulation film which is formed to cover the signal lines except for terminal portions of the signal lines in periphery of the substrate; and

conductive layers which extend in the extension direction of the signal lines ~~such that the conductive layers~~ to traverse the terminal portions, a part of each of the conductive layers is formed on the second insulating film,

wherein each of the signal lines branches to three along the extended direction to provide a central portion and two side portions ~~one two sides of the main portion,~~ and

said each conductive layer is formed on the central portion, and the second insulation film is formed on the side portions,

a pair of holes are formed among the central portion and the two side portions, and the first insulation film are exposed at positions of the holes.